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APPLICATION NO. FILING DATE FIRST NAMED INVENTOR ATTORNEY DOCKET NO. CONFIRMATION NO. Man-ho Chiang 10/808,623 03/24/2004 3409-167 6088 EXAMINER 7590 10/04/2005 Donald L. Bartels MAI, ANH T COUDERT BROTHERS LLP ART UNIT PAPER NUMBER Two Palo Alto Square 3000 El Camino Real, Fourth Floor 2832

DATE MAILED: 10/04/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

			\mathscr{A}
Office Action Summary	Application No.	Applicant(s)	$\overline{}$
	10/808,623	CHIANG ET AL.	
	Examiner	Art Unit	
	Anh T. Mai	2832	
The MAILING DATE of this communication app Period for Reply	pears on the cover sheet	with the correspondence address	s
A SHORTENED STATUTORY PERIOD FOR REPL WHICHEVER IS LONGER, FROM THE MAILING D - Extensions of time may be available under the provisions of 37 CFR 1.1 after SIX (6) MONTHS from the mailing date of this communication. - If NO period for reply is specified above, the maximum statutory period - Failure to reply within the set or extended period for reply will, by statute Any reply received by the Office later than three months after the mailin earned patent term adjustment. See 37 CFR 1.704(b).	ATE OF THIS COMMUNITY (136(a). In no event, however, may will apply and will expire SIX (6) May cause the application to become	NICATION. a reply be timely filed ONTHS from the mailing date of this commun ABANDONED (35 U.S.C. § 133).	
Status			
Responsive to communication(s) filed on 2a) ☐ This action is FINAL. 2b) ☑ This 3) ☐ Since this application is in condition for alloware closed in accordance with the practice under the practice of the practice.	s action is non-final. nce except for formal ma		rits is
Disposition of Claims			
4) ☐ Claim(s) 1-17 is/are pending in the application 4a) Of the above claim(s) is/are withdra 5) ☐ Claim(s) is/are allowed. 6) ☐ Claim(s) 1-17 is/are rejected. 7) ☐ Claim(s) is/are objected to. 8) ☐ Claim(s) are subject to restriction and/or	wn from consideration.		
Application Papers		•	
9) The specification is objected to by the Examine 10) The drawing(s) filed on is/are: a) accomposed and applicant may not request that any objection to the Replacement drawing sheet(s) including the correct 11) The oath or declaration is objected to by the Example 11.	cepted or b) objected for objected for abey drawing(s) be held in abey tion is required if the drawing.	vance. See 37 CFR 1.85(a). ng(s) is objected to. See 37 CFR 1.	
Priority under 35 U.S.C. § 119		•	
12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of: 1. Certified copies of the priority document 2. Certified copies of the priority document 3. Copies of the certified copies of the priority document application from the International Burea * See the attached detailed Office action for a list	ts have been received. ts have been received in ority documents have bee u (PCT Rule 17.2(a)).	Application No en received in this National Stag	je
Attachment(s)			
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date	Paper N	w Summary (PTO-413) lo(s)/Mail Date of Informal Patent Application (PTO-152))

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DETAILED ACTION

Claim Rejections - 35 USC § 112

- 1. The following is a quotation of the second paragraph of 35 U.S.C. 112:
 - The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.
- 2. <u>Claims 11-13</u> are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention. In claim 11, there is no antecedent basis for "the electromagnetic component".

Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 4. <u>Claims 1-2, 4-5, 7-10, 14-15</u> are rejected under 35 U.S.C. 102(b) as being anticipated by Dadafshar [US 6420953 B1]. Dadafshara discloses plurality of conductive traces having a curved shape and two terminal ends, each conductive trace formed on an insulating layer 520,525,535 of stacked PCB and positioned such that said conductive traces form a stack [c 7, ln21-32]; each said insulating layer defining an aperture 810 [figure 8], wherein each said conductive trace is shaped to substantially surround the perimeter of a respective one of said apertures [figure 7]; E-cores 510,520 positioned in the space defined by said apertures via middle portion [figs 6-7]; a copper foil 540, which has the same shape of the conductive trace with two terminal ends and a center aperture, positioned on top of the PBC stack of layers 525,530,535; a plurality of

conductors 501,502,503,504,505 for interconnecting the terminal ends of each said conductive traces to form at least one turn of a first winding and one turn of a second winding [c 6, lns 1-7; fig 5]; an additional conductor 506 for connecting at least one of said conductive layer terminal ends to a terminal end of at least one of said conductive traces, such that two windings are formed by said conductive traces and said conductive layer [figures 5-8; col 5,line 47 to col 7,line 32].

With respect to claim 2, Dadafshar discloses the top conductive layer 540 is in conductive contact with top conductive trace [col 7, lines 21-32].

With respect to claims 7-10, at least one of said conductive traces is connected by said conductors to form a first turn of said first winding, and wherein at least one of said conductive traces is connected by said conductors to form a second turn of said first winding and at least one of said conductive traces is connected by said conductors to form a first turn of said second winding, and wherein at least one of said conductive traces is connected by said conductors to form a second turn of said second winding.

With respect to claim 14, the conductors comprise at least one plated through hole 830 on the insulating layer for the connecting pins [fig 8, col 6, lines 62-64].

Claim Rejections - 35 USC § 103

- 5. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

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6. <u>Claims 6 and 16-17</u> are rejected under 35 U.S.C. 103(a) as being unpatentable over Dadafshar.

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Dadafshar discloses claimed invention [see rejection of claims 1-2] and embodiment of figure 10 wherein one or more copper plates 540A,540B can be used to give the effect of having multiple turns, each turn a separate layer. However, Dadafshar does not position the copper plate on the bottom of said stack and having two terminal ends and approximately the same shape as said conductive traces. It would have been obvious to one having ordinary skill in the art at the time the invention was made to position an additional conductive layer on the bottom of the PCB, since it has been held that rearranging parts of an invention involves only routine skill in the art to provide turn for the winding on the bottom of the PCB. *In re Japikse*, 86 USPQ 70.

7. <u>Claim 3</u> is rejected under 35 U.S.C. 103(a) as being unpatentable over Dadafshar in view of Ferencz et al. [6914508].

Dadafshar discloses the claimed invention as cited above except for the soldered attachment of the top trace to the first conductive layer. Ferencz discloses the second PCB containing the primary windings is attached to the first PCB by attachment tabs that are soldered to receiving pads of the first PCB. At the time of the invention, it would have been obvious to a person of ordinary skill in the art to solder attachment as taught by Ferencz to Dadafshar. The motivation would have been to provide connection between two electrically conductive parts. Therefore, it would have been obvious to combine Ferenzc with Dadafshar.

8. <u>Claims 11-13</u> are rejected under 35 U.S.C. 103(a) as being unpatentable over Dadafshar in view of Kobayashi [5684445].

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Dadafshar discloses the claimed invention as cited above except for an insulating layer between the top conductive plate and first conductive layer. Kobayashi discloses an insulating layer 11 placing between the windings 4,6 of the transformer [figure 1] to insulate the windings from each other. At the time of the invention, it would have been obvious to a person of ordinary skill in the art place an insulation layer between the windings as taught by Kobayshi to Dadafshar. The motivation would have been to insulate two electrical windings. Therefore, it would have been obvious to combine Ferenzc with Kobayshi.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Anh T. Mai whose telephone number is 571-272-1995. The examiner can normally be reached on 5/4/9 Schedule.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Elvin Enad can be reached on 571-272-1990. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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ANH MAI PRIMARY EXAMINER